

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L11	1	370/389.ccls. and L8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 14:30
L10	4	L1 and (URL content) near4 (access) near4 (frequency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 14:30
L9	0	L8 and (URL content) near4 (access) near4 (frequency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 14:30
L7	4	L1 and (URL content) near4 (access) near4 (frequency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 14:30
L3	1	370/389.ccls. and (flow near3 switch) and (content adj3 director)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 14:30
S33	274	(URL content) near4 (access) near4 (frequency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 14:29
L8	103	(digest near3 generator)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 14:29
S29	3	(hot near5 (URL content)) same (hit near4 counter)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 14:28
S13	34	(cache) near4 (server)near4 (cookie)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 14:28
L6	1	370/389.ccls. and L4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 14:28

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L5	3	L1 and (hot near5 (URL content)) and (hit near4 counter)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 14:28
L4	7	(flow near3 switch) and (content adj3 director)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 14:26
L2	3	709/226,238.ccls. and (flow near3 switch) and (content adj3 director)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 14:26
L1	5060	709/226,238.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 14:24
S43	2	10/004265	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/31 16:21
S42	11	S41 and S33	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/31 16:21
S41	8451	711/120,121,122,123,119,118,154,124,144,147,154,163,168.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/31 14:57
S38	117	(hash) same (digest) near5 (table store)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/31 14:56
S40	0	S38 and (cahce) and (load near4 balanc\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/31 14:39
S34	0	(URL content) near4 (access) near4 (frequency) and (cahce) and (load near4 balanc\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/31 14:39
S39	0	(hash) same (digest) near5 (table store) and S33	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/31 14:38

EAST Search History

S37	5	(content) near4 (access) near4 (frequency) and (load near4 balanc\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/31 14:37
S36	0	(content) near4 (access) near4 (frequency) and (cache) and (load near4 balanc\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/31 14:36
S35	0	(URL content) near4 (access) near4 (frequency) and (cache) and (load near4 balanc\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/31 14:36
S32	2	"6763370".PN	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/31 14:34
S31	14	("5787470" "5933849" "5935207" "6026474" "6070191" "6078960" "6112279" "6122666" "6141759" "6167427" "6167438" "6173311" "6185601" "6253230").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/05/31 14:32
S30	17	(hot same hit near4 counter)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/31 13:37
S28	4	(hot near5 (URL content)) same content near5 (re-director director)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/31 13:36
S19	3	S18 same content near5 (re-director director)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/31 13:35
S27	2	"6438652".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/31 13:29
S26	57	switch same (tag\$5 near4 mode) and (cache)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/31 13:29
S25	3	switch same (tag\$5 near4 mode) and (cache) and content near4 director	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/31 13:11

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S24	287	switch same (tag\$5 near4 mode)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/31 13:11
S23	1	switch same (tag\$5 near4 mode) same (digest\$5 near5 mode)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/31 13:10
S22	0	(flow) near3 switch same (tag\$5 near4 mode) same (digest\$5 near5 mode)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/31 13:10
S18	46811	(flow) near3 switch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/31 13:09
S21	2	"6799202".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/31 12:59
S20	6	S18 and content near5 (re-director director)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/31 12:59
S17	10	"379"/\$.ccls. and (content) near4 (based oriented) near4 (switch)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/15 09:11
S16	41	"709"/\$.ccls. and (content) near4 (based oriented) near4 (switch)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/15 09:06
S6	428	(content) near4 (based oriented) near4 (switch)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/15 09:03
S15	29	(hash near2 function) same (Location near2 key) near3 ((k) key)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/14 15:19
S14	1	(hash near2 function) same (size adj (table data)) same (location near3 key)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/14 14:05

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S8	219	(persistent) near3 (HTTP HTTPS)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/14 14:02
S9	0	Fitzsimons and 2004/0205452	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/14 12:35
S12	2	(cache) near4 (server) near4 (send\$5) near4 (cookie)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/14 11:02
S11	1	Fitzsimons and 09/932656	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/14 11:00
S10	380	Fitzsimons	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/14 10:39
S7	2	(content) near4 (based oriented) near4 (switch) and (persistent) near3 (HTTP HTTPS)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/14 09:28
S5	2	"6763370".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/14 09:00
S1	2	"6438652".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/13 16:52
S4	18	(cache)near2 (server) near3 (farm)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/13 16:45
S3	3	(content) near2 (cache)near2 (servers) and (load near3 balanc\$5) and (digest) near4 (generator)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/13 16:45
S2	4	(content) near2 (cache)near2 (servers) and (load) near3 balanc\$5) and (traffic near3 monitor) and (cach\$5 near4 (criteria policy)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/13 16:26

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IEEE JNL IEE Journal or Magazine

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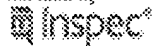
IEEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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1 [Fast instruction cache performance evaluation using compile-time analysis](#)



David B. Whalley

 June 1992 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1992 ACM SIGMETRICS joint international conference on Measurement and modeling of computer systems SIGMETRICS '92/PERFORMANCE '92,**
 Volume 20 Issue 1

Publisher: ACM Press

 Full text available: [pdf\(1.08 MB\)](#)

 Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)
Keywords: cache simulation, instruction cache, trace analysis, trace generation

2 [Improving I/O performance with a conditional store buffer](#)



Lambert Schaelicke, Al Davis

 November 1998 **Proceedings of the 31st annual ACM/IEEE international symposium on Microarchitecture**

Publisher: IEEE Computer Society Press

 Full text available: [pdf\(2.53 MB\)](#)

 Additional Information: [full citation](#), [references](#), [index terms](#)

3 [TouchCounters: designing interactive electronic labels for physical containers](#)



Paul Yarin, Hiroshi Ishii

 May 1999 **Proceedings of the SIGCHI conference on Human factors in computing systems: the CHI is the limit**

Publisher: ACM Press

 Full text available: [pdf\(1.42 MB\)](#)

 Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We present TouchCounters, an integrated system of electronic modules, physical storage containers, and shelving surfaces for the support of collaborative physical work. Through physical sensors and local displays, TouchCounters record and display usage history information upon physical storage containers, thus allowing access to this information during the performance of real-world tasks. A distributed communications network allows this data to be exchanged with a server, such that us ...

Keywords: distributed sensing, tangible interfaces, ubiquitous computing, visualization

4 Extending graphics hardware for occlusion queries in OpenGL ☐

 Dirk Bartz, Michael Meißner, Tobias Hüttner

August 1998 **Proceedings of the ACM SIGGRAPH/EUROGRAPHICS workshop on Graphics hardware**

Publisher: ACM Press

Full text available:  pdf(953.96 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

Keywords: OpenGL, hierarchical data structures, occlusion culling, visibility

5 Prediction caches for superscalar processors ☐

James E. Bennett, Michael J. Flynn

December 1997 **Proceedings of the 30th annual ACM/IEEE international symposium on Microarchitecture**

Publisher: IEEE Computer Society

Full text available:  pdf(1.02 MB)  Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)
[Publisher Site](#)

Processor cycle times are currently much faster than memory cycle times, and this gap continues to increase. Adding a high speed cache memory allows the processor to run at full speed, as long as the data it needs is present in the cache. However, memory latency still affects performance in the case of a cache miss. Prediction caches use a history of recent cache misses to predict future misses and to reduce the overall cache miss rate. This paper describes several prediction caches, and introdu ...


Keywords: Dynamic scheduling, Memory latency, Stream buffer, Victim cache, Prediction cache

6 AIDE - a tool for computer architecture design ☐

D. J. Ellenberger, Y. W. Ng

June 1981 **Proceedings of the 18th conference on Design automation**

Publisher: IEEE Press

Full text available:  pdf(678.48 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

AIDE (Architecture Design Environment) is a modeling and simulation system designed to support the development of computer architectures. By providing a modular, hierarchical modeling environment plus interactive simulation and performance evaluation capabilities, AIDE facilitates the critical analysis necessary in top-down architecture designs. The system currently runs under the UNIX* operating system on a VAX** 11/780. This paper presents the organization of AIDE and discusses its applic ...

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